

SRI VENKATESWARA COLLEGE OF ENGINEERING (AUTONOMOUS)

Karakambadi Road, Tirupati-517507

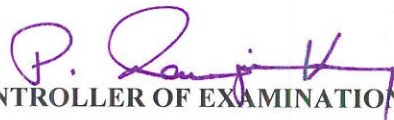
EXAMINATION BRANCH

M.Tech I Semester (R20) Supplementary Examinations May-2023 TIME TABLE

Time: **FN: 10.00 A.M TO 01.00 P.M**

Date/Day	Power Systems (PS)	Advanced Manufacturing Systems (AMS)	VLSI Design (VLSI D)	Computer Science and Engineering (CSE)
22-05-2023 (Monday)	Power Quality EE20DPC101	Modern Machining Processes ME20DPC101	CMOS Analog IC Design EC20DPC101	Mathematical Foundations of Computer Science CS20DPC101
24-05-2023 (Wednesday)	Advanced Power System Protection EE20DPC102	Automation in Manufacturing ME20DPC102	CMOS Digital IC Design EC20DPC102	Advanced Data Structures CS20DPC102
26-05-2023 (Friday)	Solar Energy Conversion Systems EE20DPE104	Advanced Casting and Welding Technology ME20DPE104	Nanomaterials & Nanotechnology EC20DPE104	Machine Learning CS20DPE103
29-05-2023 (Monday)	Energy Auditing & Management EE20DPE106	Design for Assembly and Manufacturing ME20DPE108	CPLD and FPGA Architectures and Applications EC20DPE107	Data Science CS20DPE106
31-05-2023 (Wednesday)	Research Methodology and IPR EE20DOE109	Research Methodology and IPR ME20DPC111	Research Methodology and IPR EC20DPC109	Research Methodology and IPR CS20DPC109

Note: Any discrepancy in this time table may be brought to the notice of the under signed immediately.


CONTROLLER OF EXAMINATIONS


PRINCIPAL

Date: 08-05-2023